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11-5-02  
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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q57124

Tae-yong SOHN

Appln. No.: 09/472,869

Group Art Unit: 2614

Confirmation No.: 9316

Examiner: NATNAEL, PAULOS M.

Filed: December 28, 1999

For: APPARATUS FOR SELECTIVELY CONVERTING CLOCK FREQUENCY IN  
DIGITAL SIGNAL RECEIVER

*9/11/02*  
AMENDMENT UNDER 37 C.F.R. § 1.111

RECEIVED

Commissioner for Patents  
Washington, D.C. 20231

OCT 31 2002

Technology Center 2600

Sir:

In response to the Office Action dated July 30, 2002, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please enter the following amended claims:

The claims are amended as follows:

1. (Amended) An apparatus for selectively converting a clock frequency in a digital signal receiver, comprising:

a first phase locked loop;

a second phase locked loop;

a switching portion for selecting a clock frequency from one of the first and second phase locked loops according to a predetermined control signal;